

PATENT ABSTRACTS OF JAPAN

(11)Publication number : 01-151854

(43)Date of publication of application : 14.06.1989

(51)Int.Cl.

H04L 11/02

H04L 13/00

(21)Application number : 62-311229

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(22)Date of filing : 09.12.1987

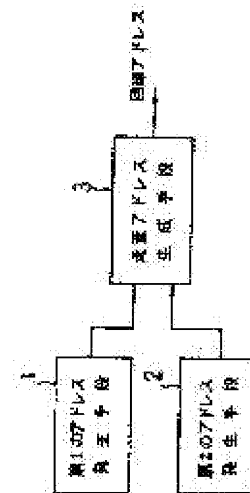
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(54) LINE SCANNING SYSTEM

(57)Abstract:

PURPOSE: To distribute a prescribed scanning capability or over to all lines regardless of any scan control to accommodate a high speed circuit by scanning a line having a line address generated by a scanning address generating means sequentially.

CONSTITUTION: The scanning address generating means 3 generates a line address to allow a line scanning mechanism to scan by outputting selectively a line address outputted from the 1st and 2nd address generating means 1, 2 alternately. The line scanning mechanism scans sequentially a line having a line address generated by the scanning address generating means 3. Thus, a prescribed value or over the scanning capability is distributed to all the lines independently of the scan control implemented to accommodate a high speed line.



LEGAL STATUS

[Date of request for examination]

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's decision of rejection]

[Date of extinction of right]